

**REMARKS**

The Office Action mailed March 21, 2007, has been received and reviewed. Claims 5 through 19 are currently pending in the application. Claims 5 through 19 stand rejected. Applicant has amended claims 5, 6, and 11, and respectfully request reconsideration of the application as amended herein.

**Information Disclosure Statements**

Please note that two Information Disclosure Statements were filed herein on September 26, 2003, and while we appreciate receiving an initialed copy of the PTO/SB/08A that was returned with the Office Action mailed March 21, 2007, we have not received an initialed copy of the PTO-1449 that was also filed on September 26, 2003. Applicant respectfully requests that the information cited on the PTO-1449 be made of record herein. For the sake of convenience, a second copy of the September 26, 2003, Information Disclosure Statement, PTO-1449 with copy of USPTO date-stamped postcard and Express Mail receipt are enclosed herewith. It is respectfully requested that an initialed copy of the PTO-1449 evidencing consideration of the cited references be returned to the undersigned attorney.

**Double Patenting Rejection Based on U.S. Patent No. 6,549,821, U.S. Patent No. 6,593,171, U.S. Patent No. 6,890,801 and U.S. Patent No. 6,909,929**

Claims 11 through 19 stand rejected under the judicially created doctrine of obviousness-type double patenting for reciting subject matter that is allegedly unpatentable over the subject matter recited in claims 1, 8, and 9 of U.S. Patent No. 6,549,821 B1, claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 B2, claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 B2, and claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 B2.

An obviousness-type double patenting rejection is appropriate where the subject matter recited in a claim is merely an obvious variation of the invention recited in a claim of an issued or patent or pending patent application. M.P.E.P. § 804.

A double patenting rejection of the obviousness-type is 'analogous to [a failure to meet] the nonobviousness requirement of 35 U.S.C. 103' except that the patent principally underlying the

double patenting rejection is not considered prior art. *In re Braithwaite*, 379 F.2d 594, 154 USPQ 29 (CCPA 1967). Therefore, any analysis employed in an obviousness-type double patenting rejection parallels the guidelines for analysis of a 35 U.S.C. 103 obviousness determination. *In re Braat*, 937 F.2d 589, 19 USPQ2d 1289 (Fed. Cir. 1991); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985). M.P.E.P. § 804.

It is respectfully submitted that the obviousness-type double patenting rejections of claims 11-19 are inappropriate.

U.S. Patent 6,549,821

Claims 1, 8 and 9 of U.S. Patent 6,549,821 are directed to a method of packaging a semiconductor die.

It is respectfully submitted that the subject matter to which claims 1, 8 and 9 of U.S. Patent 6,549,821 is directed does not support a *prima facie* case of obviousness against any of claims 11 through 19 of the above-referenced application. In particular, it is respectfully submitted that none of claims 1, 8 and 9 of U.S. Patent 6,549,821 recites (or teaches or suggests) adhering a plurality of electronic components in fixed positions to one side of a film *suspended in* a frame member and securing the frame member to a platform within a stereolithography apparatus, as recited in independent claim 11 of the above-referenced application. Moreover, it is respectfully submitted that claims 1, 8 and 9 of U.S. Patent 6,549,821 would not provide one of ordinary skill in the art with any motivation to modify the methods thereof to include adhering electronic components to a film suspended in a frame member and securing the frame member to a platform.

Therefore, it is respectfully submitted that the subject matter recited in independent claim 11 of the above-referenced application is allowable over the subject matter to which claims 1, 8 and 9 of U.S. Patent 6,549,821 is directed.

Each of claims 12-19 is allowable, among other reasons, for depending from independent claim 11, which is allowable.

Each of claims 12-19 is additionally allowable as the cited claims of the cited reference do not recite (or teach or suggest) inverting a frame member and releasing a plurality of

electronic components from a film.

U.S. Patent No. 6,593,171

Claims 11 through 19 stand rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 B2

Claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 are directed to a method of fabricating a substantially hermetic package.

It is respectfully submitted that the subject matter to which claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 is directed does not support a *prima facie* case of obviousness against any of claims 5 through 19 of the above-referenced application. In particular, it is respectfully submitted that none of claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 recites (or teaches or suggests) adhering a plurality of electronic components in fixed positions to one side of a film *suspended in* a frame member and securing the frame member to a platform within a stereolithography apparatus, as recited in independent claim 11 of the above-referenced application. Moreover, it is respectfully submitted that claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 would not provide one of ordinary skill in the art with any motivation to modify the methods thereof to include adhering electronic components to a film suspended in a frame member and securing the frame member to a platform.

Therefore, it is respectfully submitted that the subject matter recited in independent claim 11 of the above-referenced application is allowable over the subject matter to which claims 1, 4 through 7, 14, and 15 of U.S. Patent No. 6,593,171 is directed.

Each of claims 12-19 is allowable, among other reasons, for depending from independent claim 11, which is allowable.

Each of claims 12-19 is additionally allowable as the cited claims of the cited reference do not recite (or teach or suggest) inverting a frame member and releasing a plurality of electronic components from a film.

U.S. Patent No. 6,890,801

Claims 11 through 19 stand rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 B2.

Claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 are directed to a method of fabricating a substantially hermetic package.

It is respectfully submitted that the subject matter to which claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 is directed does not support a *prima facie* case of obviousness against any of claims 11 through 19 of the above-referenced application. In particular, it is respectfully submitted that none of claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 recites (or teaches or suggests) adhering a plurality of electronic components in fixed positions to one side of a film *suspended in* a frame member and securing the frame member to a platform within a stereolithography apparatus, as recited in independent claim 11 of the above-referenced application. Moreover, it is respectfully submitted that claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 would not provide one of ordinary skill in the art with any motivation to modify the methods thereof to include adhering electronic components to a film suspended in a frame member and securing the frame member to a platform.

Therefore, it is respectfully submitted that the subject matter recited in independent claim 11 of the above-referenced application is allowable over the subject matter to which claims 1, 5 through 8, 15, and 16 of U.S. Patent No. 6,890,801 is directed.

Each of claims 12-19 is allowable, among other reasons, for depending directly from independent claim 11, which is allowable.

Each of claims 12-19 is additionally allowable as the cited claims of the cited reference do not recite (or teach or suggest) inverting a frame member and releasing a plurality of electronic components from a film.

U.S. Patent No. 6,909,929

Claims 11 through 19 stand rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 B2.

Claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 are directed to a method of fabrication of articles.

It is respectfully submitted that the subject matter to which claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 is directed does not support a *prima facie* case of obviousness against any of claims 5 through 19 of the above-referenced application. In particular, it is respectfully submitted that none of claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 recites (or teaches or suggests) adhering a plurality of electronic components in fixed positions to one side of a film suspended in a frame member and securing the frame member to a platform within a stereolithography apparatus, as recited in independent claim 11 of the above-referenced application. Moreover, it is respectfully submitted that claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 would not provide one of ordinary skill in the art with any motivation to modify the methods thereof to include adhering electronic components to a film suspended in a frame member and securing the frame member to a platform.

Therefore, it is respectfully submitted that the subject matter recited in independent claims 5 and 11 of the above-referenced application is allowable over the subject matter to which claims 1, 5 through 7, 11, and 12 of U.S. Patent No. 6,909,929 is directed.

Each of claims 12-19 is allowable, among other reasons, for depending from independent claim 11, which is allowable.

Each of claims 12-19 is additionally allowable as the cited claims of the cited reference do not recite (or teach or suggest) inverting a frame member and releasing a plurality of electronic components from a film.

**35 U.S.C. § 112 Claim Rejections**

Claims 5 through 10 stand rejected under 35 U.S.C. § 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to reasonably convey

to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention, and as failing to comply with the enablement requirement.

Additionally, claims 5 and 10 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Applicant respectfully traverses these rejections, as hereinafter set forth.

Claims 5 and 6 have been amended herein to recite in part “each of the plurality of electronic component assemblies including electronic components exposed through a cavity within the platen assembly.” Support for this amendment is found in paragraphs [0053]-[0054], and [0059] of the as-filed specification.

### **35 U.S.C. § 102(e) Anticipation Rejections**

#### Anticipation Rejection Based on U.S. Patent No. 6,549,821 to Farnworth et al.

Claims 11 through 19 stand rejected under 35 U.S.C. § 102(e) as being anticipated by Farnworth et al. (U.S. Patent No. 6,549,821). Applicant respectfully traverses this rejection, as hereinafter set forth.

A claim is anticipated only if each and every element as set forth in the claim is found, either expressly or inherently described, in a single prior art reference. *Verdegaal Brothers v. Union Oil Co. of California*, 2 USPQ2d 1051, 1053 (Fed. Cir. 1987). The identical invention must be shown in as complete detail as is contained in the claim. *Richardson v. Suzuki Motor Co.*, 9 USPQ2d 1913, 1920 (Fed. Cir. 1989).

Farnworth et al. discloses a stereolithographic method and apparatus for applying packaging material to workpieces. A plurality of dice 100 (FIGS. 6A and 6B) are secured and electrically connected to a lead frame 132 of a lead frame strip 130. Alternatively, a plurality of dice 100 (FIGS. 7) may be secured and electrically connected to a carrier substrate 200 to create a multi-chip module 1000. The individual dice 100 may be encapsulated, using stereolithography to form layers of material around the periphery of the dice. *See Farnworth et al.* at col. 12, line 45 through col. 13, line 47.

Independent claim 11, as amended herein, recites “A method of processing electronic

components, comprising: adhering a plurality of electronic components in fixed positions to one side of a film suspended in a frame member; securing the frame member to a platform within a stereolithography apparatus with the plurality of electronic components adhered to the one side of the film facing upward; forming at least one stereolithographic structure adjacent each of the plurality of electronic components adhered to the one side of the film; and removing the frame member from the stereolithography apparatus.”

Farnworth et al. fails to expressly or inherently describe adhering a plurality of electronic components in fixed positions to one side of a film suspended in a frame member. Rather, Farnworth et al. describes electrically connecting a plurality of dice 100 to either a lead frame 132 of a lead frame strip 130 or a carrier substrate 200 to create a multi-chip module 1000. Neither the lead frame nor the carrier substrate comprises a film *suspended in* a frame member.

Applicants respectfully assert, therefore, that independent claim 11 is not anticipated by Farnworth et al. under 35 U.S.C. § 102(e) at least because claim 11 recites “adhering a plurality of electronic components in fixed positions to one side of a film suspended in a frame member.” Farnworth et al. does not expressly or inherently describe adhering a plurality of electronic components in fixed positions to one side of a film suspended in a frame member. Thus, applicants respectfully request that the Examiner withdraw the rejection of independent claim 11 under 35 U.S.C. § 102(e).

Claims 12-19 are each allowable, among other reasons, as depending from claim 11, which should be allowed.

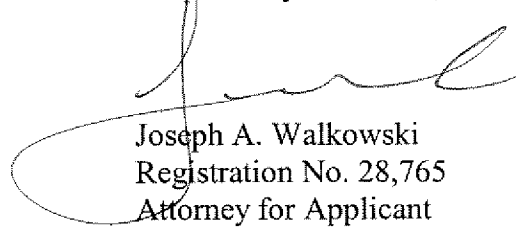
**ENTRY OF AMENDMENTS**

The amendments to claims 5, 6 and 11 above should be entered by the Examiner because the amendments are supported by the as-filed specification and drawings and do not add any new matter to the application.

**CONCLUSION**

Claims 5 through 19 are believed to be in condition for allowance, and an early notice thereof is respectfully solicited. Should the Examiner determine that additional issues remain which might be resolved by a telephone conference, he is respectfully invited to contact Applicant's undersigned attorney.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'J. Walkowski', is written over the typed name and title.

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Date: June 20, 2007

KLD/sfc:slm

Document in ProLaw



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Warren M. Farnworth

**Serial No.:** Not Yet Assigned

**Filed:** September 26, 2003

**For:** APPARATUS AND METHODS FOR  
USE IN STEREOLITHOGRAPHIC  
PROCESSING OF COMPONENTS AND  
ASSEMBLIES

**Confirmation No.:** Unknown

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 2269-3996.2US  
(99-0254.02/US)

NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EV326922169US

Date of Deposit with USPS: September 26, 2003

Person making Deposit: Chris Haughton

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

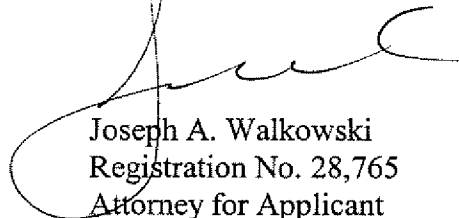
The present application is a divisional of application Serial No. 09/944,488, filed August 30, 2001, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the merits that such art has in fact been reviewed. A PTO-1449 or PTO/SB/08 form listing all of the information of record in the prior application is enclosed herewith.

**Attorney Docket No. 2269-3996.2US**

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: September 26, 2003

JAW/dlm:ljb

Enclosures: Form PTO-1449 or PTO/SB/08

Document in ProLaw

<b>Form PTO-1449</b>  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <i>(Use several sheets if necessary)</i>	Docket Number (Optional) <b>3996.2US (99-0254.02/US)</b>	Application Number <b>Not Yet Assigned</b>
	Applicant <b>Warren M. Farnworth</b>	
	Filing Date <b>September 26, 2003</b>	Group Art Unit <b>Unknown</b>

### U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,173,220	12/1992	Reiff et al.			
	5,264,061	11/1993	Juskey et al.			
	5,484,314	01/1996	Farnworth			
	5,705,117	01/1998	O'Connor et al.			

### FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

### OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)


EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

THE PATENT & TRADEMARK OFFICE MAILROOM DA  
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Utility Patent Application Transmittal (1 page); Fee Transmittal (1 page, with duplicate copy); Check No. 5017 in the amount of \$834.00; Specification including title page, claims and single page abstract (35 pages); Formal Drawings (5 sheets, 8 figures); Copy of Declaration for Patent Application (with Power of Attorney) (1 page); Information Disclosure Statement (2 pages); and Form PTO-1449 (1 page); Supplemental Information Disclosure Statement (2 pages); and Form PTO/SB/08 (1 page) with attached references.

Invention: APPARATUS AND METHODS FOR USE IN  
STEREOLITHOGRAPHIC PROCESSING OF  
COMPONENTS AND ASSEMBLIES

Applicant(s): Warren M. Farnworth

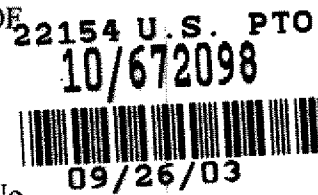
Filing Date: September 26, 2003

Serial No.: Not Yet Assigned

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